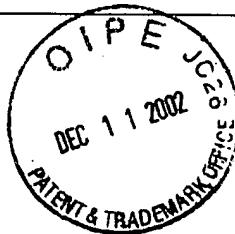


IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

WARREN M. FARNWORTH
SALMAN AKRAM

Serial No.: 09/844,532



EXAMINER: Chang, R.

Art Unit: 3729

Filing Date: April 30, 2001

For: METHOD FOR FABRICATING A TEST INTERCONNECT
FOR BUMPED SEMICONDUCTOR COMPONENTS BY
FORMING RECESSES AND CANTILEVERED LEADS ON A SUBSTRATE

Attorney Docket No. 97-1433.1

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.56
December 5, 2002

Assistant Commissioner of Patents
BOX RCE
Washington, DC 20231

Dear Sir:

The citations listed on the accompanying form PTO/SB/08, copies attached, may be material to the examination of the subject application and are therefore submitted in compliance with the duty of disclosure defined in 37 CFR §§1.56, 1.97 and 1.98.

This Information Disclosure Statement under 37 CFR §§1.56, 1.97 and 1.98 is not to be construed a representation that a search has been made, that additional material to the examination of this application does not exist, or that any one or more of these citations constitutes prior art under 35 USC §102.

It is requested that the above citations be made of record, if appropriate, in the prosecution of this application.

Respectfully submitted:

Stephen A. Grattan, Registration No. 28,418
Attorney for Applicants

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CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, postage prepaid, in an envelope addressed to: Assistant Commissioner of Patents, BOX RCE, Washington, DC. 20231 on this 5th day of December, 2002.

December 5, 2002
Date of Signature

Stephen A. Grattan, Attorney for Applicants

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